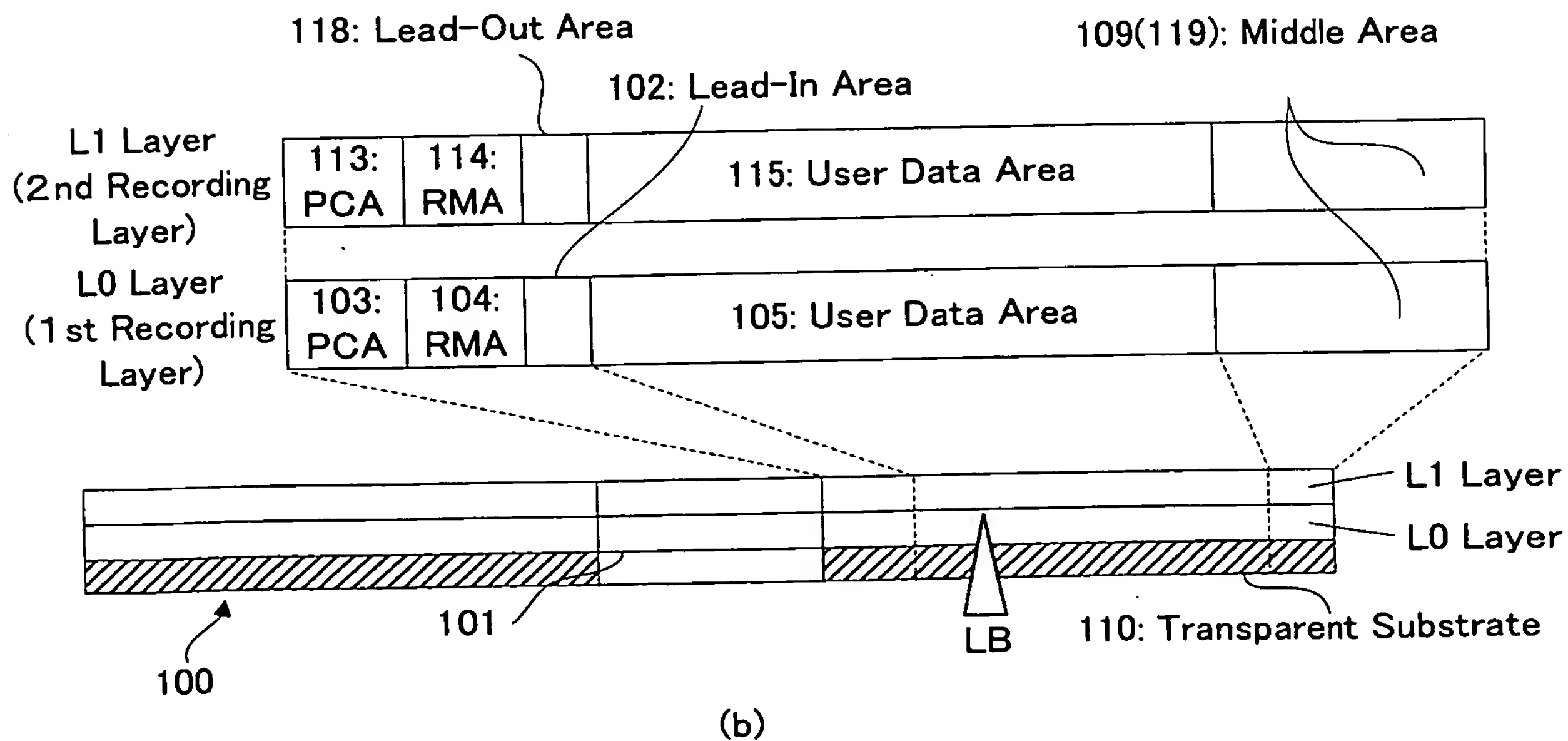
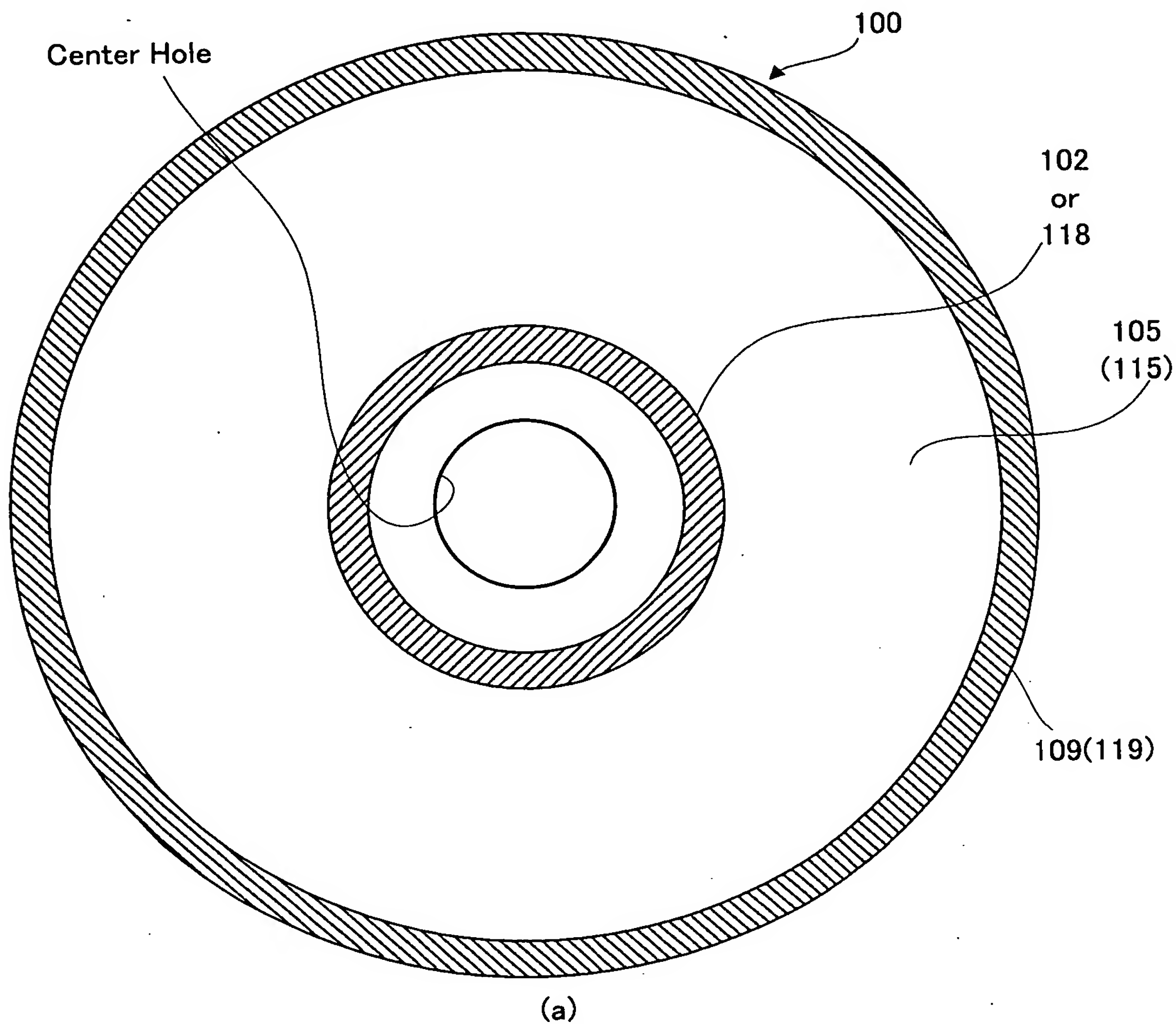
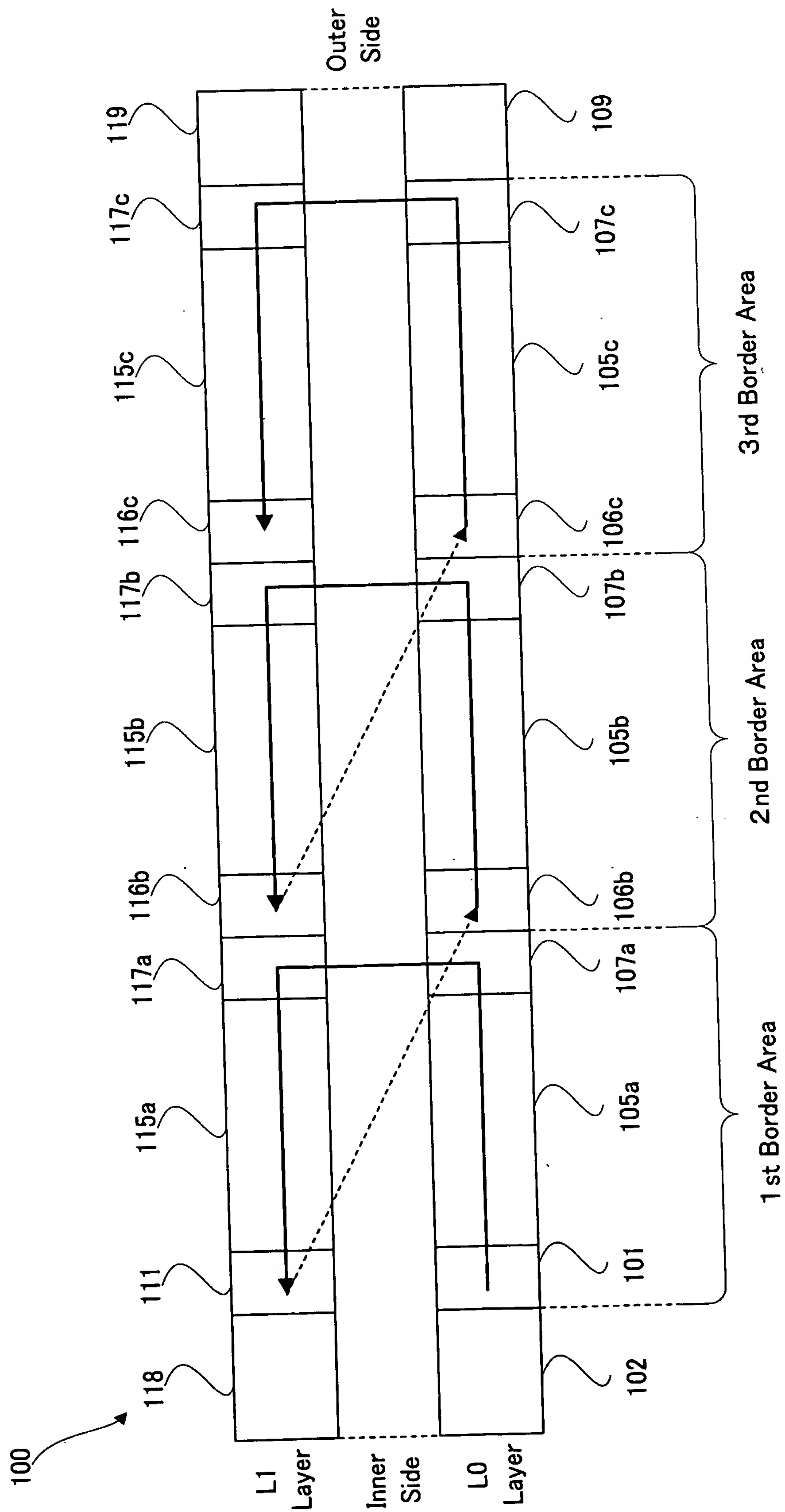


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[FIG. 1]



[FIG. 2]



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[FIG. 3]

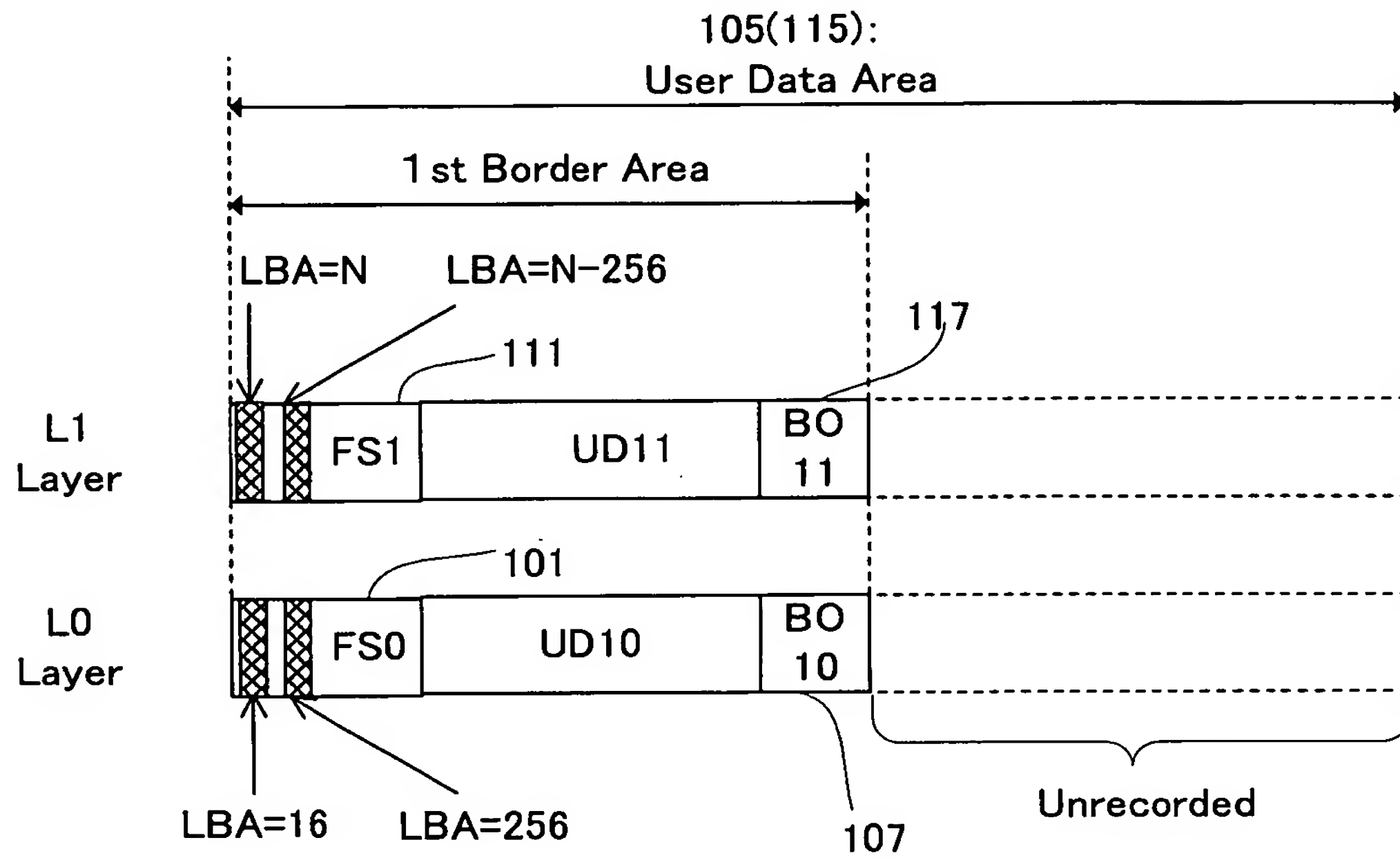
Byte Position	Content	121
0 to 3	Update Block Sector Pointer (AP # 1)	
4 to 7	Update Block Sector Pointer (AP # 2)	
8 to 11	Update Block Sector Pointer (AP # 3)	
12 to 15	Update Block Sector Pointer (AP # 4)	121
16 to 31	Reserved	
32 to 35	Start Sector No. (Border Out: L0)	
36 to 39	Start Sector No. (Border Out: L1)	
40 to end	Other	

[FIG. 4]

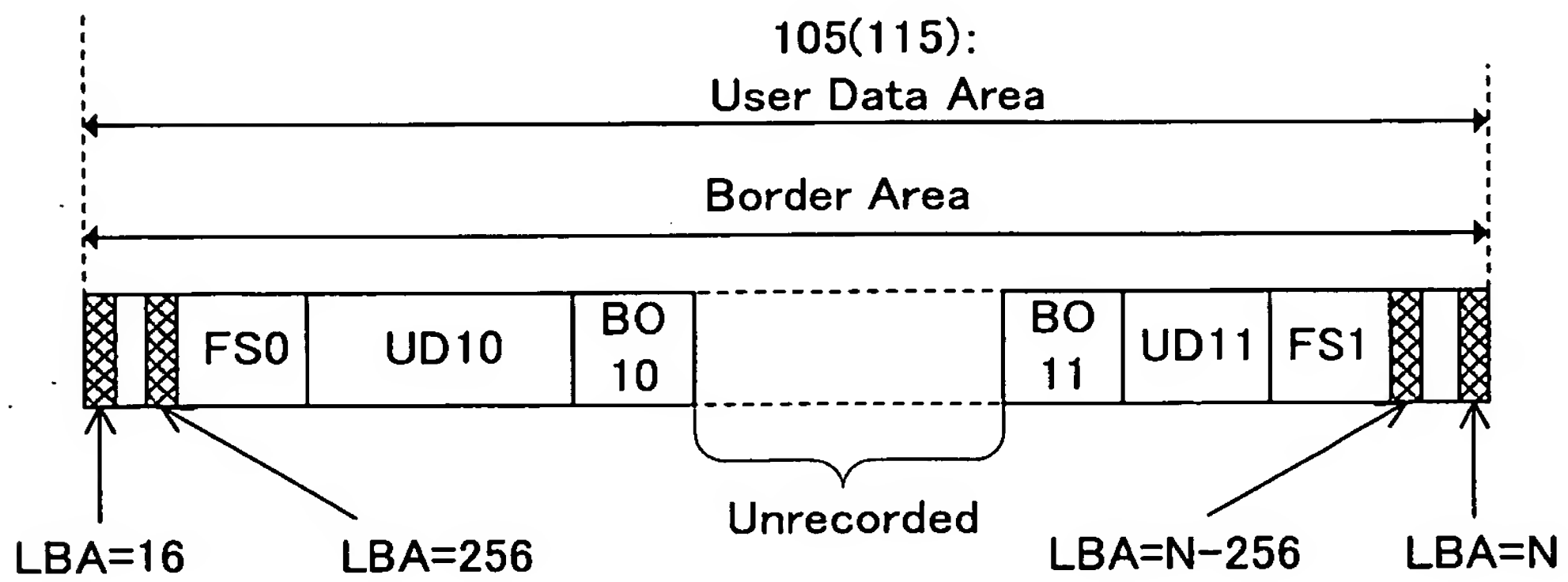
Anchor Point	Logical Block Address	Content
AP#1	16h	VRS
AP#2	256h	AVDP
AP#3	LRA-256h	AVDP
AP#4	LRA	VAT_ICB

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[FIG. 5]



(a)



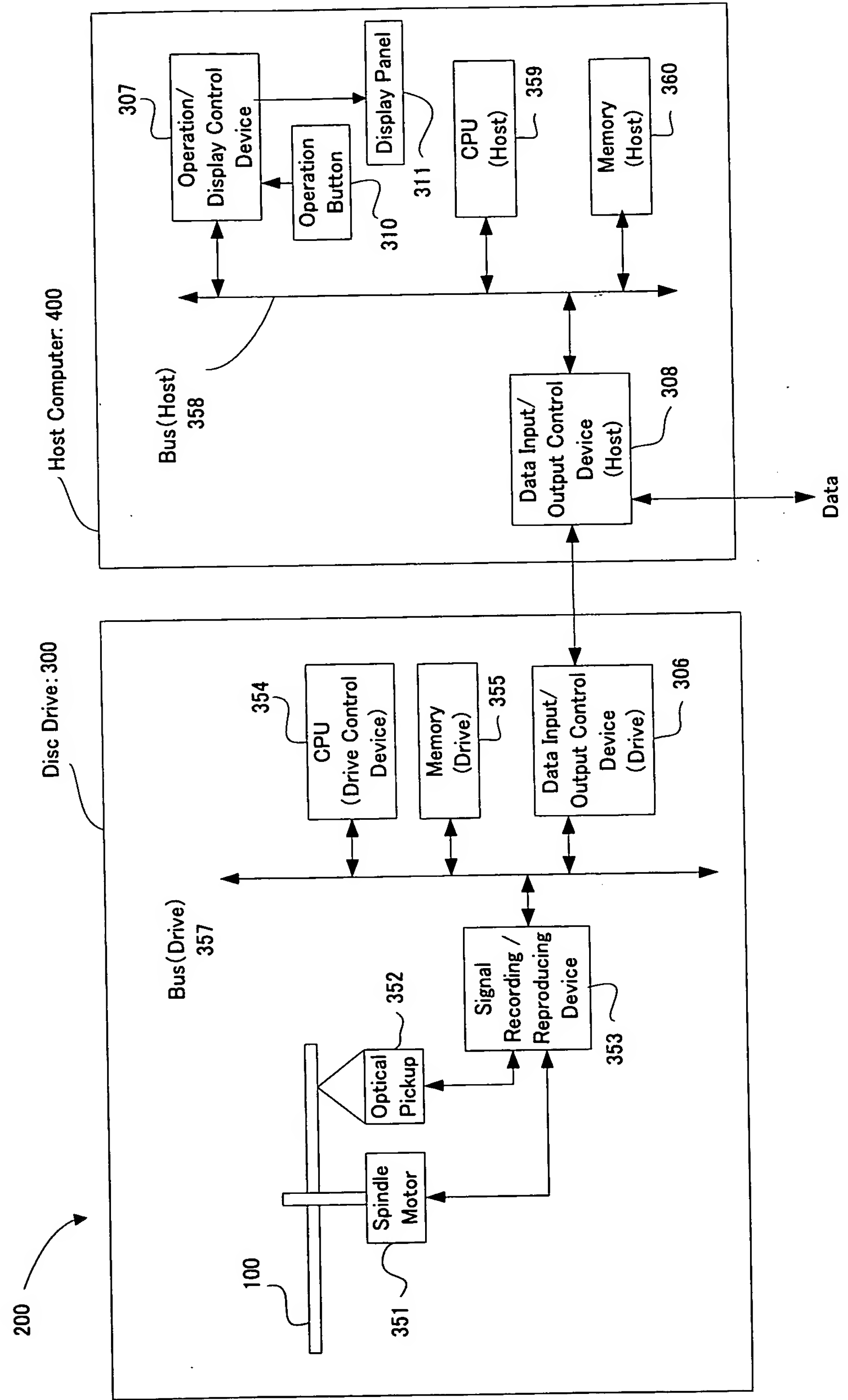
(b)

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[FIG. 6]

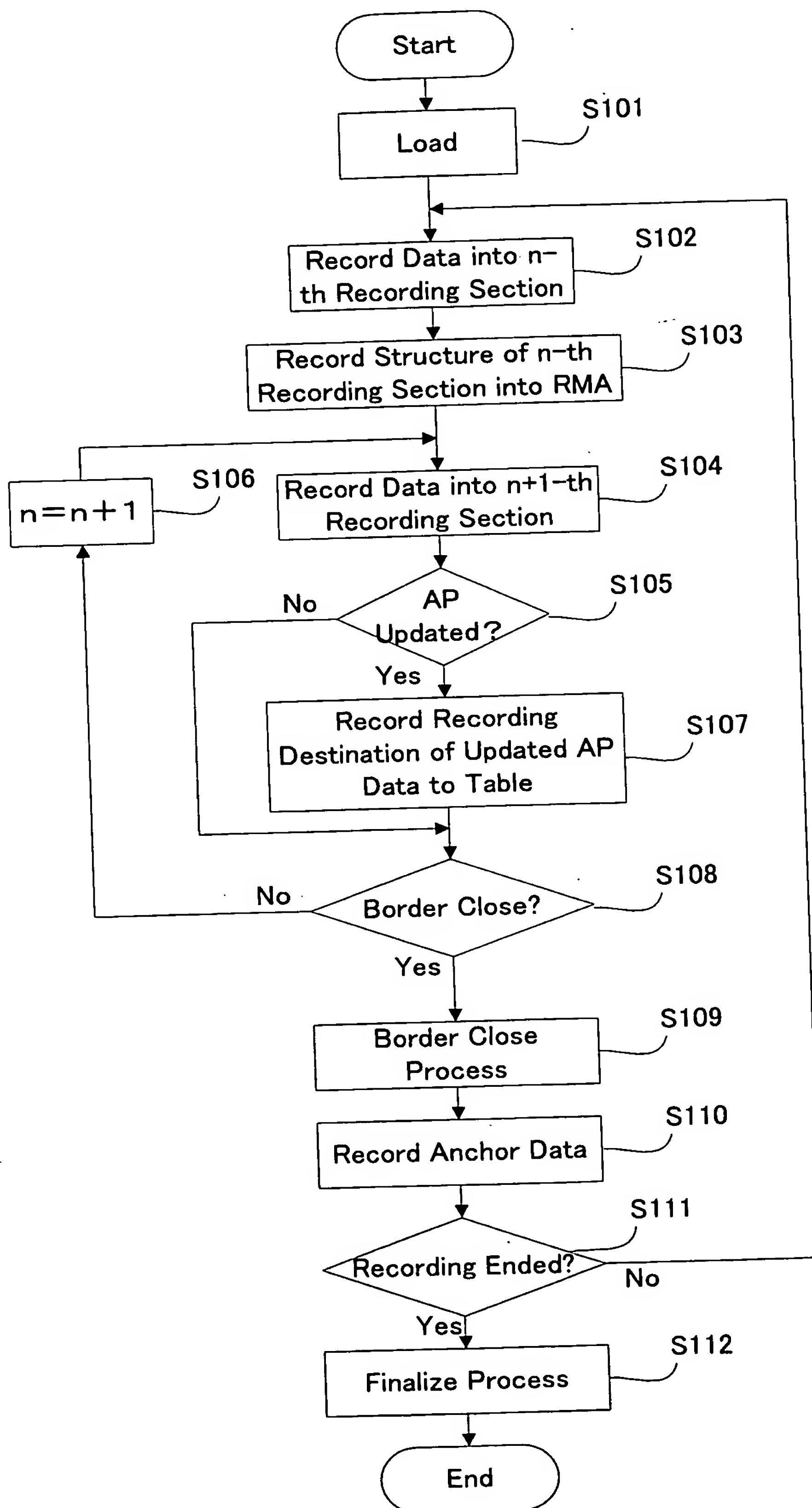
Bit Position	Content	131
0	Update Block Sector Effective Flag (AP # 1)	
1	Update Block Sector Effective Flag (AP # 2)	
2	Update Block Sector Effective Flag (AP # 3)	
3	Update Block Sector Effective Flag (AP # 4)	131
4 to 7	Reserved	

[FIG. 7]



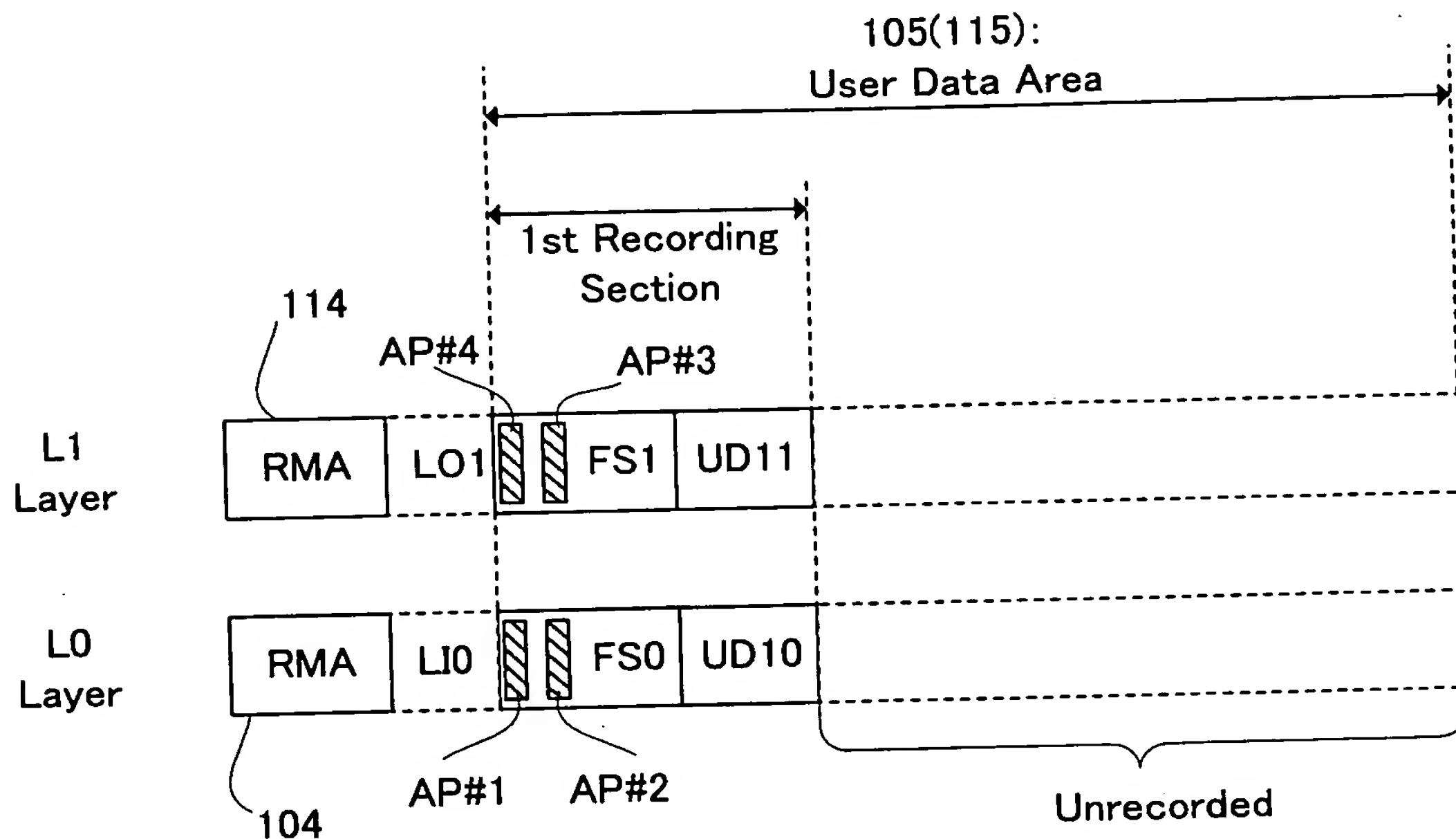
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[FIG. 8]

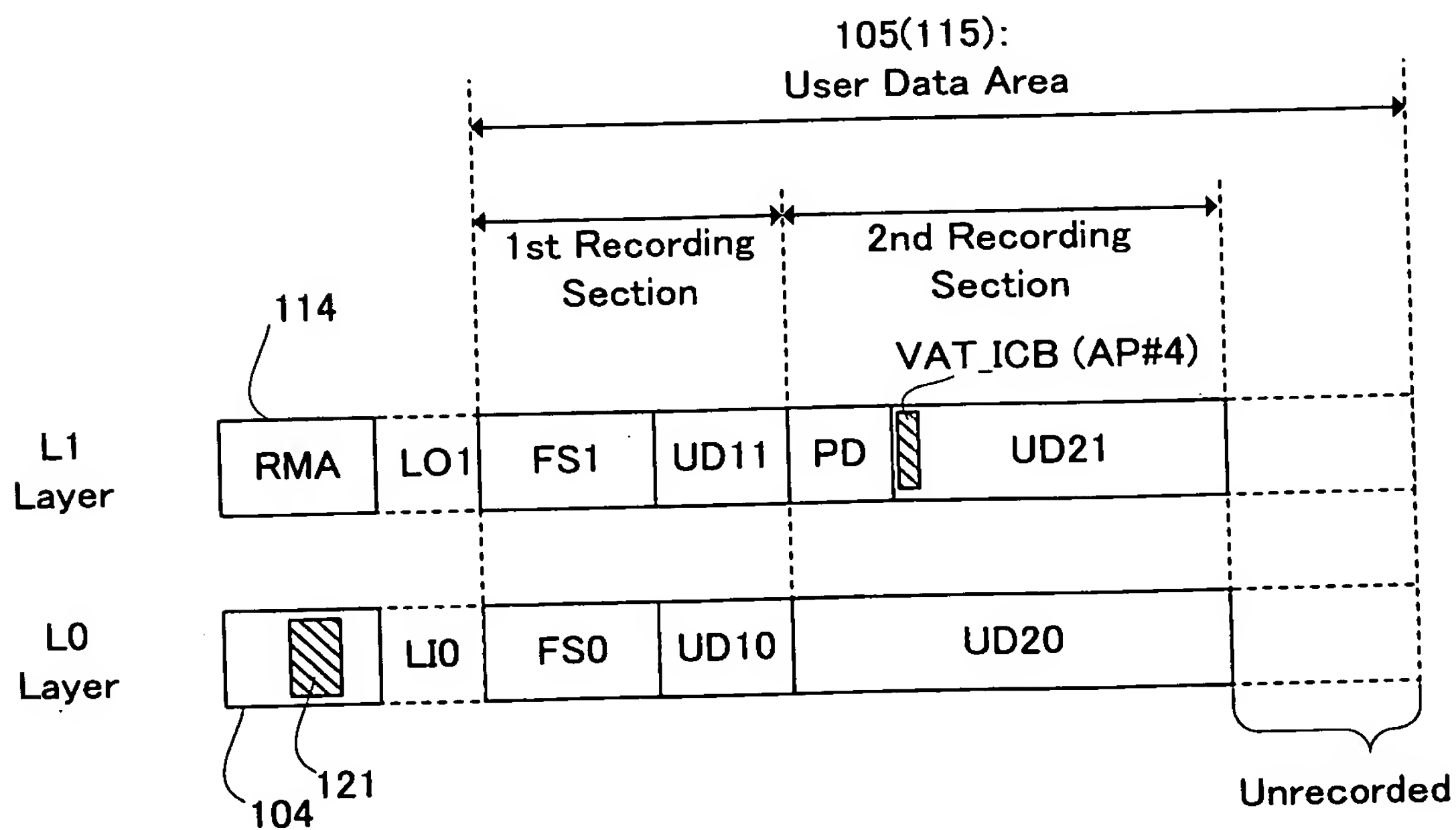


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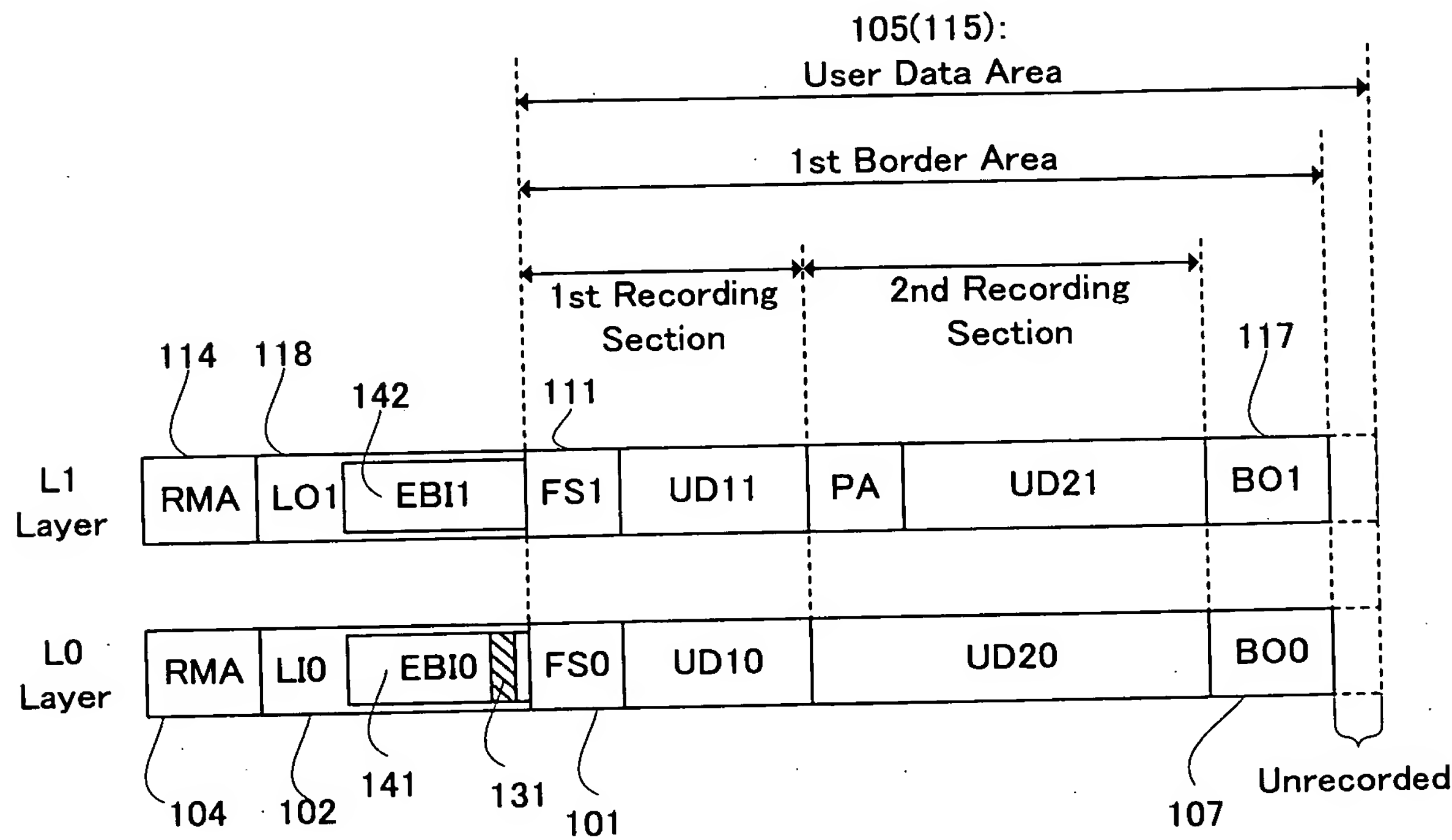
[FIG. 9]



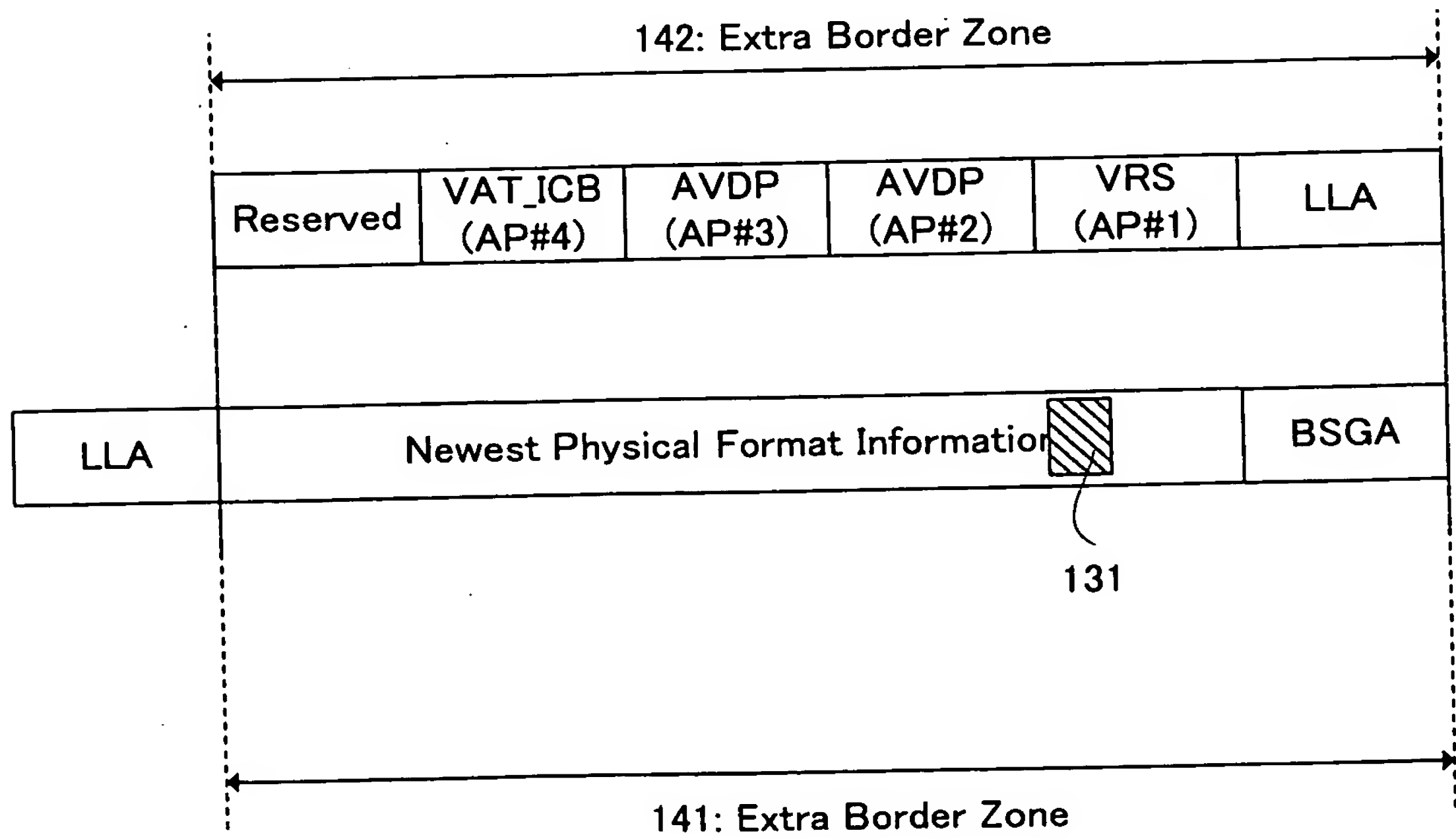
[FIG. 10]



[FIG. 11]

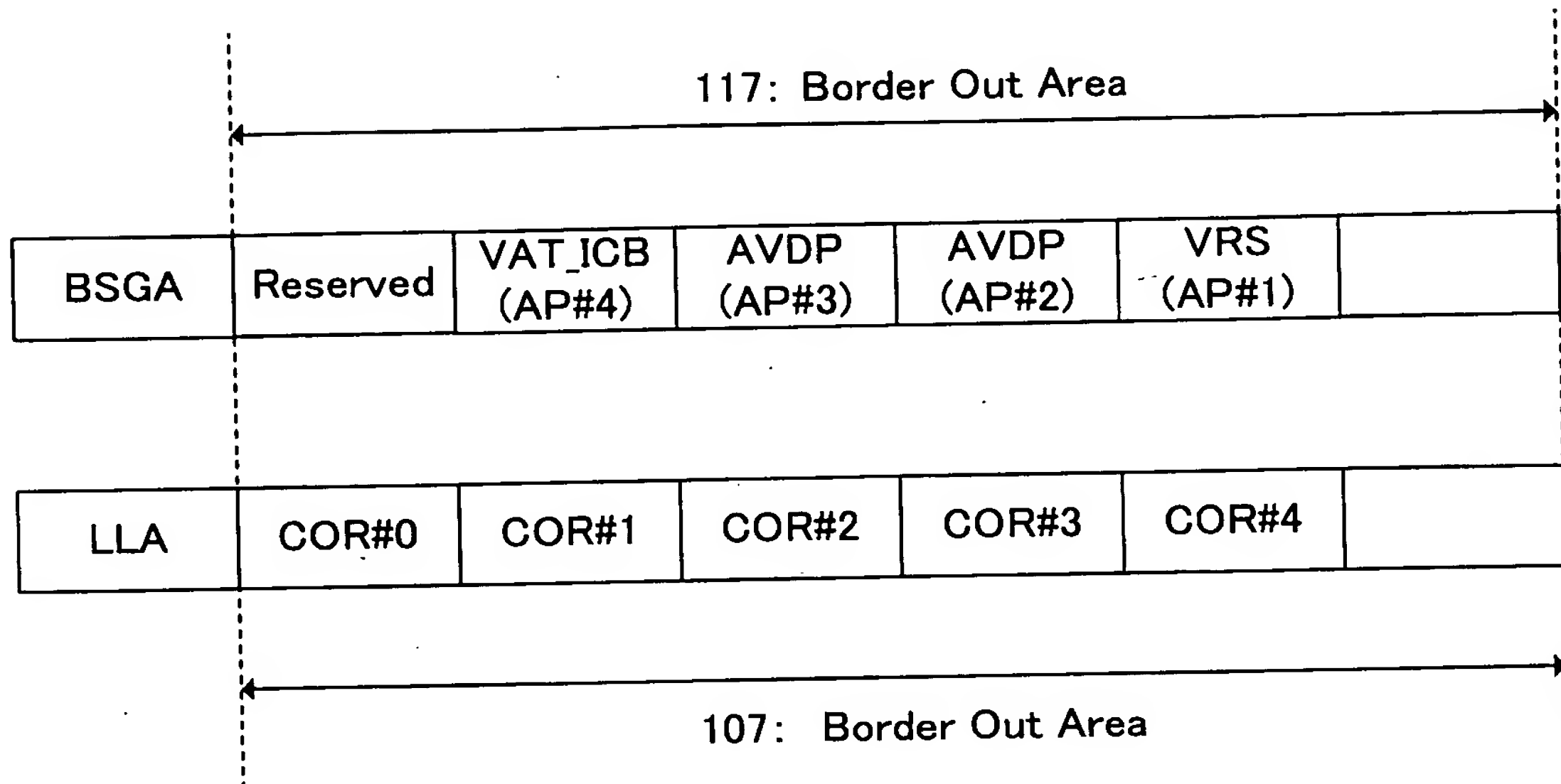


[FIG. 12]

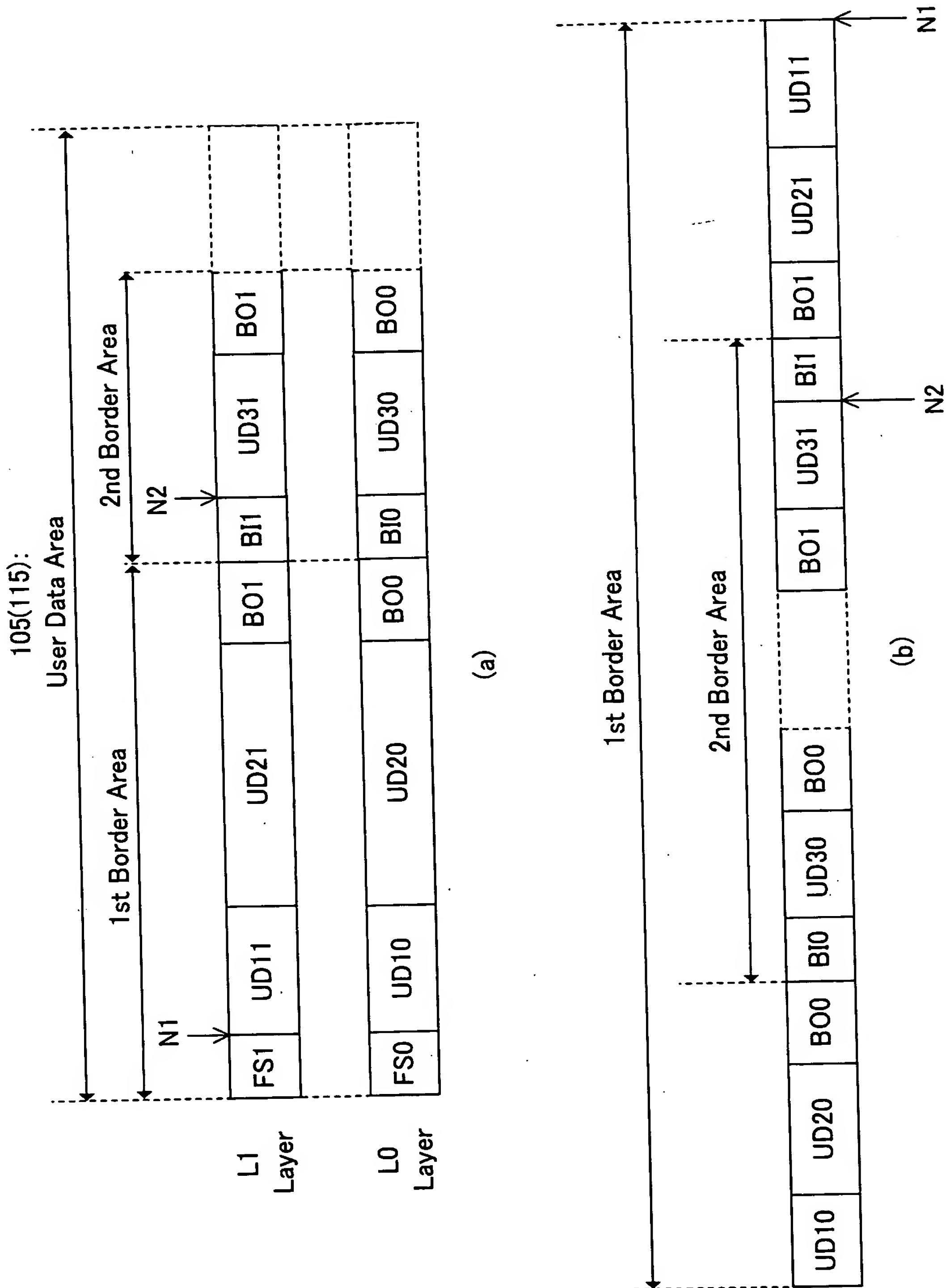


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[FIG. 13]

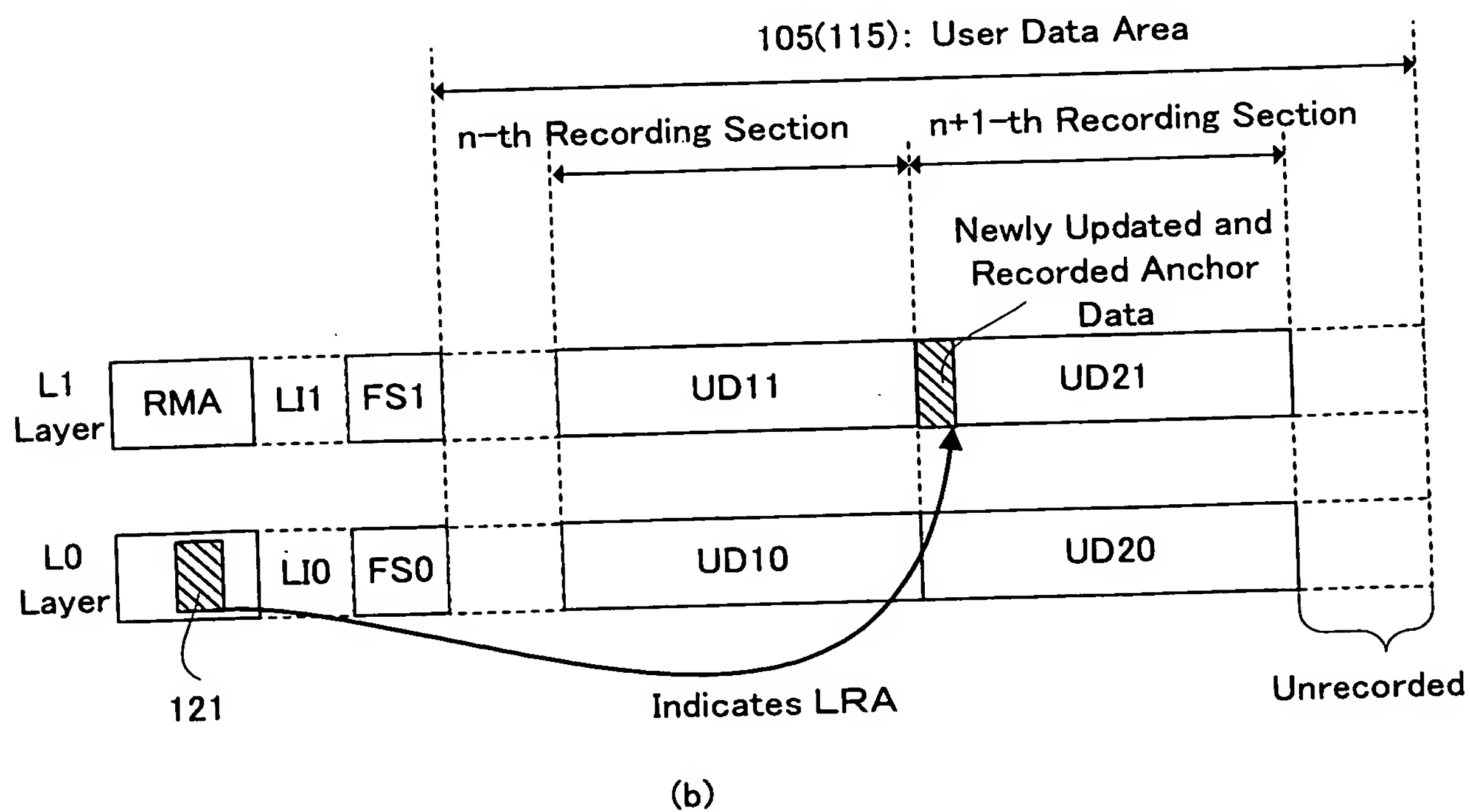
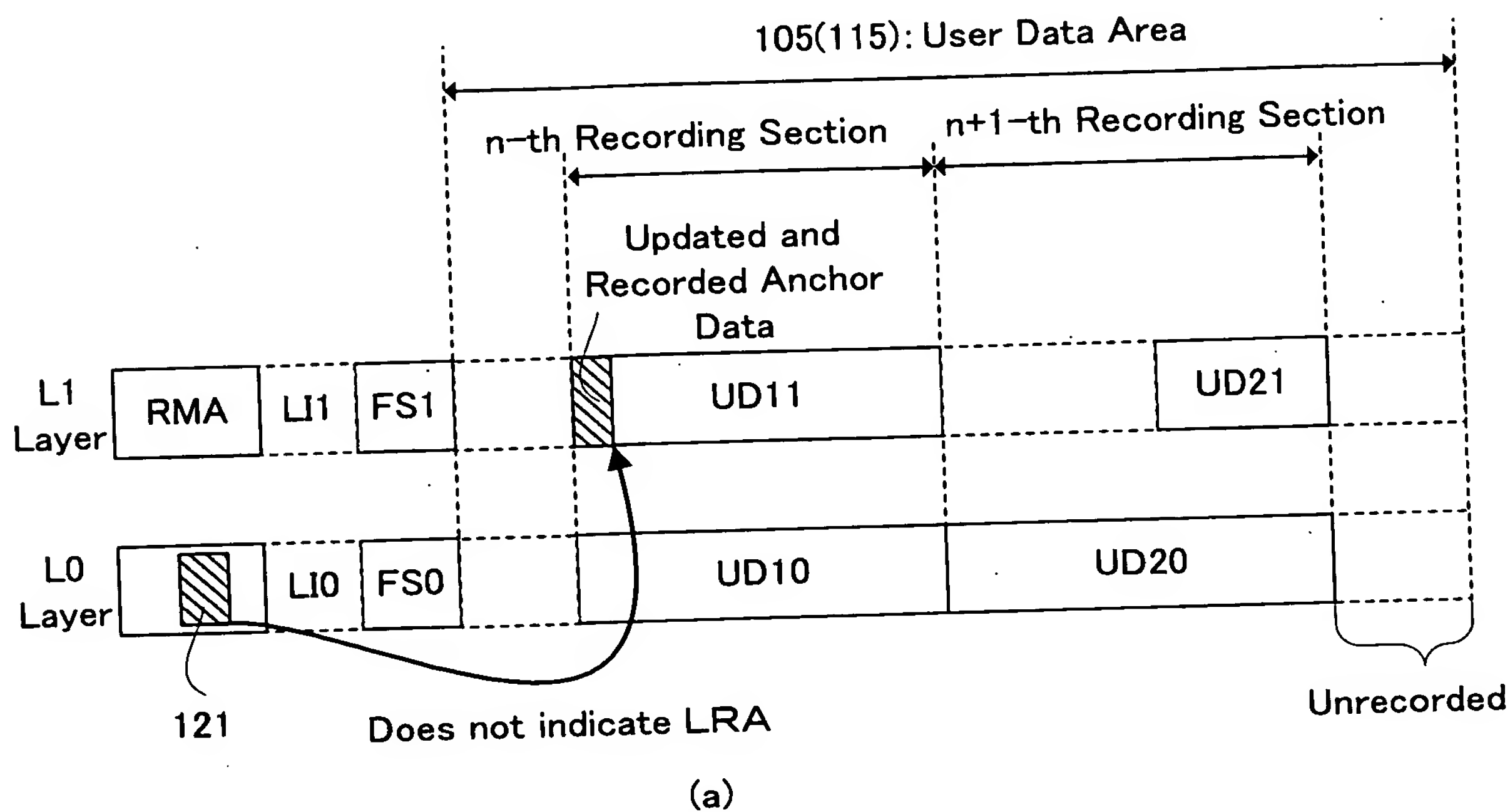


[FIG. 14]



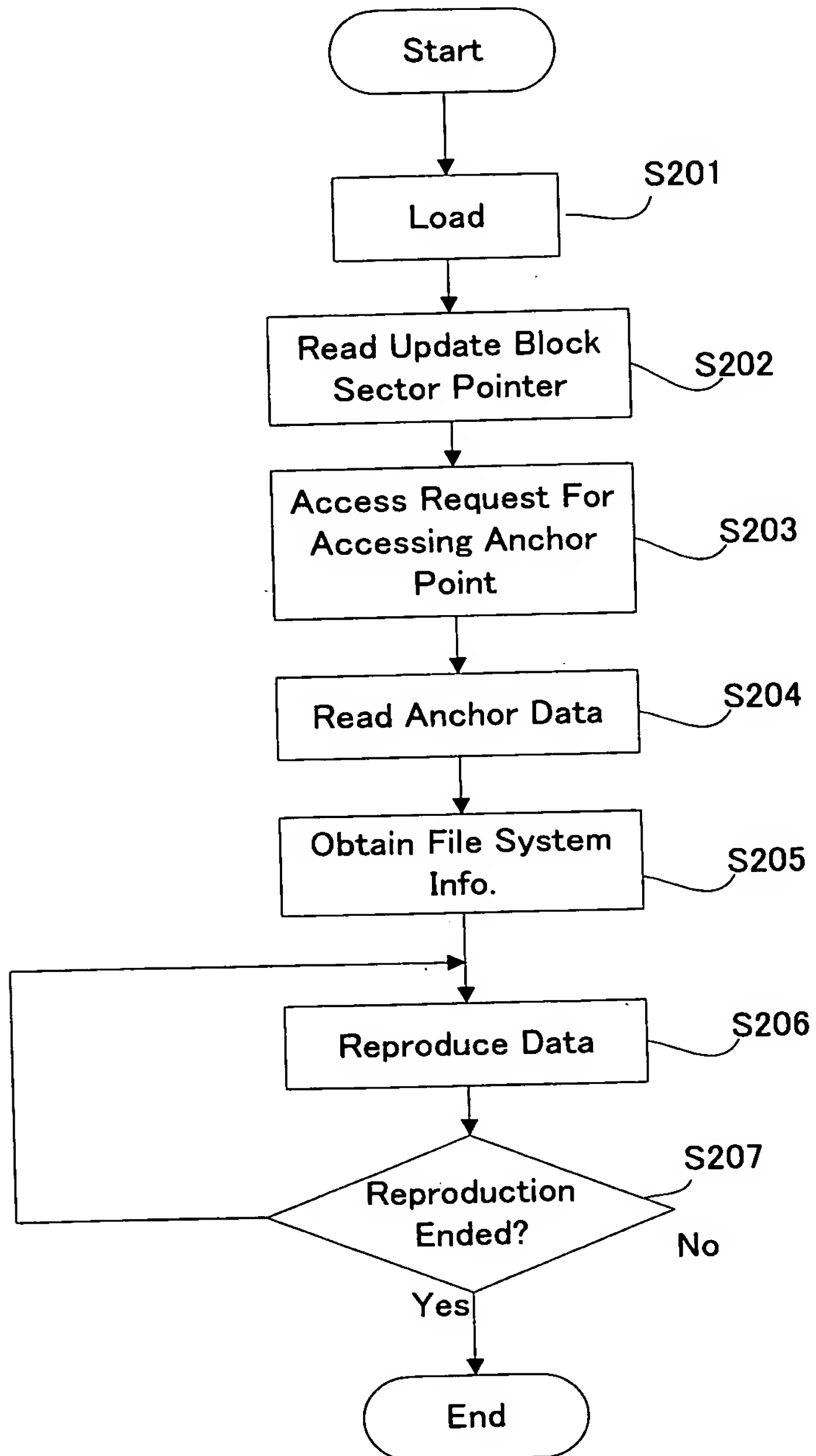
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[FIG. 15]



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[FIG. 16]



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[FIG. 17]

Byte Position	Content	
0 to 3	Update Block Original Sector Address (AP # 1)	122
4 to 7	Update Block Original Sector Address (AP # 2)	
8 to 11	Update Block Original Sector Address (AP # 3)	
12 to 15	Update Block Original Sector Address (AP # 4)	
16 to 19	Update Block Sector Pointer (AP # 1)	121
20 to 23	Update Block Sector Pointer (AP # 2)	
24 to 27	Update Block Sector Pointer (AP # 3)	
28 to 31	Update Block Sector Pointer (AP # 4)	
32 to 35	Start Sector No. (Border Out # 1)	
36 to 39	Start Sector No. (Border Out # 2)	
40 to end	Other	